

[Sign in](#)[Web](#) [Images](#) [Video](#) ^{New!} [News](#) [Maps](#) [more »](#)

compile endian

Search

[Advanced Search](#)
[Preferences](#)**Web**Results 11 - 20 of about 709,000 for compile endian. (0.06 seconds)[PDF] A Proposal for **Endian**-Portable Record Representation ClausesFile Format: PDF/Adobe Acrobat - [View as HTML](#)

endian processes of a biendian machine). Another is the **compile**-time problem of specifying a data layout portably, so that a single version of an Ada ...

www.ada-auth.org/ai-files/grab_bag/bitorder.pdf - [Similar pages](#)

Linux-Kernel Archive: **compile** a big-**endian** kernel on a prep machine

i want to **compile** the kernel in big-**endian** orderring. i saw : processor.h --> MSR_LE, MSR_ILE byteorder.h mkprep.h can you give me advices about it ? ...

www.ussg.iu.edu/hypermail/linux/kernel/9808.1/0669.html - 3k - [Cached](#) - [Similar pages](#)

Linux-Kernel Archive: Re: **compile** a big-**endian** kernel on a prep ...

In reply to: Herve REY: "**compile** a big-**endian** kernel on a prep machine" ... i want to **compile** the kernel in big-**endian** orderring. ...

www.ussg.iu.edu/hypermail/linux/kernel/9808.1/1084.html - 3k - [Cached](#) - [Similar pages](#)

Sun Tools & Products Forums - **Compile** flag for the **endian** neutral ...

Is any **compile** flag applied to convert the little **endian** to big **endian** to save my life for the porting? I am using Sun Studio 11 with Sun C 5.8 Patch ...

forum.sun.com/jive/thread.jspa?threadID=99144&messageID=340061 - 28k -

[Cached](#) - [Similar pages](#)

Using the GNU **Compiler** Collection (GCC)

Note: this option should only be used if you require compatibility with code for big-**endian** ARM processors generated by versions of the **compiler** prior to ...

gcc.gnu.org/onlinedocs/gcc-3.2.3/gcc/ARM-Options.html - 16k -

[Cached](#) - [Similar pages](#)

Re: How do I build universal binaries for libraries with **endian** ...

Re: How do I build universal binaries for libraries with **endian**-specific code ... have **endian**-specific code that is included or removed at **compile** time. ...

lists.apple.com/archives/Xcode-users/2005/Sep/msg00805.html - 31k -

[Cached](#) - [Similar pages](#)

[Ffmpeg-devel] ffmpeg cross-**compile** for big-**endian** armv5

[Ffmpeg-devel] ffmpeg cross-**compile** for big-**endian** armv5. John Clonts john at clonts.org Fri May 26 05:47:13 CEST 2006. Previous message: [Ffmpeg-devel] ...

lists.mplayerhq.hu/pipermail/ffmpeg-devel/2006-May/011379.html - 10k -

[Cached](#) - [Similar pages](#)

[Ffmpeg-devel] ffmpeg cross-**compile** for big-**endian** armv5

[Ffmpeg-devel] ffmpeg cross-**compile** for big-**endian** armv5. John Clonts john at clonts.org Thu May 25 02:23:35 CEST 2006. Previous message: [Ffmpeg-devel] Re: ...

lists.mplayerhq.hu/pipermail/ffmpeg-devel/2006-May/011317.html - 7k -

[Cached](#) - [Similar pages](#)

[[More results from lists.mplayerhq.hu](#)]

port-sgimips: kernel **compile**: big endian not recognized properly

Subject: kernel **compile**: big endian not recognized properly ... locore.o: compiled for a big endian system and target is little endian File in wrong format: ...

mail-index.netbsd.org/port-sgimips/2001/10/26/0006.html - 3k - [Cached](#) - [Similar pages](#)

SDSC User Services : IA-64 Linux Cluster User Guide : **Compiling**

This enables the development and processing of files with big-endian data ... X

compiler) allow the display of source file traceback information at runtime ...

www.sdsc.edu/user_services/ia64/**compile**.html - 19k - [Cached](#) - [Similar pages](#)



Result Page: [Previous](#) [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [11](#) [Next](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google

[Sign in](#)[Web](#) [Images](#) [Video](#) ^{New!} [News](#) [Maps](#) [more »](#)

endian munging

Search

[Advanced Search](#)
[Preferences](#)**Web**

Results 1 - 10 of about 12,500 for endian munging. (0.05 seconds)

[Roland Caßeböhm - No address **munging** for platforms don't need to](#)

Little-endian version -#if (CYG_BYTEORDER == CYG_LSBFIRST) +// Little-endian version or big-endian version that doesn't need address **munging** +#if ...
ecos.sourceforge.org/ml/ecos-patches/2003-11/msg00016.html - 11k -
[Cached](#) - [Similar pages](#)

[BYTE.com](#)

To accommodate smaller scalars, a technique known as address **munging** is used. When the PowerPC is in little-endian mode, it transforms the 3 low-order bits ...
www.byte.com/art/9509/sec12/art1.htm - 34k - [Cached](#) - [Similar pages](#)

[\[PDF\] MPC8xx True Little-Endian Mode](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)
Big-Endian Mode. SDMA byte lane swapper. BO field of FCR. 1x. PowerPC
Little-Endian Mode. Load-Store address **munging** (3 bits) ...
www.freescale.com/files/archives/doc/app_note/MPC823LEM.pdf - [Similar pages](#)

[Broadcom BCM94306](#)

It's more complex than this, because it is pseudo little endian, **munging** low address bits. The memory seen as an array of bytes is not invariant through ...
ozlabs.org/pipermail/linuxppc-dev/2004-January/016293.html - 6k -
[Cached](#) - [Similar pages](#)

[Broadcom BCM94306](#)

... because it is pseudo little endian, > >**munging** > >low address bits. ... Well, if the processors switched to little endian with byte invariant addressing ...
ozlabs.org/pipermail/linuxppc-dev/2004-January/016319.html - 7k -
[Cached](#) - [Similar pages](#)
[[More results from ozlabs.org](#)]

[Mark Salter - Re: Big endian ARM HAL WRITE UINT8 problem](#)

As an example: If little or big endian, in both systems the receive buffer is > at ... IMO, the address **munging** is only necessary on badly designed (broken! ...
sources.redhat.com/ml/ecos-discuss/2002-07/msg00414.html - 6k -
[Cached](#) - [Similar pages](#)

[Terme](#)

... as little-endian values, when in fact it is stored in big-endian order, ... Note that **munging** affects only the effective address and not the byte order. ...
doc-telecom.enst-bretagne.fr/doc-telecom/terme.jsp?485 - 3k - [Cached](#) - [Similar pages](#)

[Interbus buffer for use between a pseudo little endian bus and a ...](#)

Address **munging** is a solution that reorders data transferred from big endian devices to little endian devices. This solution combines two steps. ...
www.freepatentsonline.com/5627975.html - 57k - [Cached](#) - [Similar pages](#)

MR Brown - Re: SH Linux: remove big endian multilib

Re: SH Linux: remove big endian multilib ... disable multilib for (and there's no way to pick a default for these besides **munging** the spec/fragment files). ...

gcc.gnu.org/ml/gcc-patches/2001-07/msg02059.html - 5k - [Cached](#) - [Similar pages](#)

Writing PCI Drivers: Byte-Invariant Addressing vs. Data Structure ...

Most non-Intel-based computers are big endian. This presents a number of issues ...

These issues mostly deal with **munging** data between the PCI bus and the ...

developer.apple.com/.../DeviceDrivers/Conceptual/

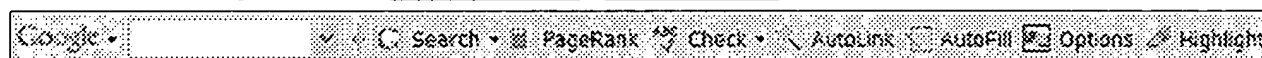
WritingPCIDrivers/endianness/chapter_7_section_3.html - 14k -

[Cached](#) - [Similar pages](#)

Google

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

Free! Get the Google Toolbar. [Download Now](#) - [About Toolbar](#)



endian munging

Search

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google

[craigs.list.jason@gmail.com](#) | [Search History](#) | [My Account](#) | [Sign out](#)[Web](#) [Images](#) [Video](#) ^{New!} [News](#) [Maps](#) [more »](#)

binary translation endian

Search

[Advanced Search](#)
[Preferences](#)[Turn OFF Personalized Search \(Beta\) for these results »](#)**Web**Personalized Results 1 - 10 of about **273,000** for **binary translation endian**. (0.07 seconds)[From gorton@spf64.amt.tay1.dec.com Wed Oct 18 15:35:30 1995 Path ...](#)It is also Digital's first **binary translator** product to convert big-endian executables into little-endian executables. * VEST = DECmigrate for OpenVMS ...[www.uruk.org/emu/gorton.txt](#) - 20k - [Cached](#) - [Similar pages](#)[\[PDF\] A Dynamic Binary Translation Approach to Architectural Simulation](#)File Format: PDF/Adobe Acrobat - [View as HTML](#)by performing dynamic **binary translation** of the PowerPC instruction set architecture ... The **endian** mode is inherited from the host running the simulator. ...[www.cs.wisc.edu/~cain/pubs/wbt_cain.pdf](#) - [Similar pages](#)[\[PS\] Dynamic Binary Translation](#)File Format: Adobe PostScript - [View as Text](#)code is a problem for a **binary translator** and presents. solutions. Finally, section 9 briefer ... and the Alpha, while big-endian machines include the ...[www.complang.tuwien.ac.at/schani/papers/bintrans-ukuug2002.ps.gz](#) - [Similar pages](#)[endian : Java Glossary](#)It might export directly in either big-endian **binary** DataOutputStream or character DataOutputStream format. See **binary** formats. Write a separate **translator** ...[mindprod.com/jgloss/endian.html](#) - 38k - Aug 31, 2006 - [Cached](#) - [Similar pages](#)[\[PDF\] Microsoft PowerPoint - 12-binary-translation-3](#)File Format: PDF/Adobe Acrobat - [View as HTML](#)For **binary translation** or predecoding, holding the TPC in a register may also be useful ... This is referred to as big **endian** (or network) byte order ...[www-user.tu-chemnitz.de/~mien/materialien/sys/12-binary-translation-3.pdf](#) -[Similar pages](#)[pack - convert a list into a binary representation](#)N A long in "network" (big-endian) order. v A short in "VAX" ... Such bugs lie in the automatic **translation** process that automatically created the hundreds ...[www.perl.com/doc/manual/html/pod/perlfunc/pack.html](#) - 8k - [Cached](#) - [Similar pages](#)[\[PDF\] SECURITY APPLICATIONS OF DYNAMIC BINARY TRANSLATION by DINO DAI ...](#)File Format: PDF/Adobe Acrobat - [View as HTML](#)5 Security Approaches Using Dynamic **Binary Translation**. 28. 5.1 Out-Of-Band

Return Address Verification In our running example, we assume a big-endian ...

[www.cs.unm.edu/~moore/tr/02-12/zovi.pdf](#) - [Similar pages](#)[Most likely to amaze: The author wishes to remain anonymous Great ...](#)Warning: note that the **translator** screws around with the **binary** you run on ...Automatic **Endian** Detection/Reversal: There are various problems raised when ...[www.ioccc.org/2001/anonymous.hint](#) - 7k - [Cached](#) - [Similar pages](#)

Andrew Cagney - Re: gdb -> OcdLibRemote -> Raven | Wiggler ...

It put on to the address/data busses: Address: 4 (in binary 100) Data Lane: 0: 1: 2: 3: 4:
A 5: B 6: C 7: D For a little **endian** store (same address and ...
sources.redhat.com/ml/gdb/2003-09/msg00256.html - 11k - [Cached](#) - [Similar pages](#)

Converting and Porting SPICE **Binary** Data Files ...

The breakdown by computing environment is as follows: Big **Endian** IEEE: ... The
binary translation capability provides ``read-only'' access to non-native ...
sohowwww.nascom.nasa.gov/solarsoft/stereo/gen/exe/icy/doc/convert.ug - 18k -
[Cached](#) - [Similar pages](#)

Goooooooooooooogle ►

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

Free! Speed up the web. [Download the Google Web Accelerator.](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google

craigs.list.jason@gmail.com | [Search History](#) | [My Account](#) | [Sign out](#)[Web](#) [Images](#) [Video](#) ^{New!} [News](#) [Maps](#) [more »](#)

address space swapping endian

Search

[Advanced Search](#)
[Preferences](#)**Web**Results 1 - 10 of about 228,000 for address space swapping endian. (0.19 seconds)Initialization - 2:52pm

You must specify the **address space** and data width of the device you wish to ... These functions include a flag that can be set to "swap" **Endian** styles. ...

flywheel.princeton.edu/~wfisher/links/VMEIL_useage.html - 6k -

[Cached](#) - [Similar pages](#)

VMEIL.h File Reference

Closes an existing connection and deallocates **address space** associated with the ... **swap**, Set to 1 for **Endian** byte **swapping**, set to 0 for no **swapping**. ...

flywheel.princeton.edu/~wfisher/VMEIL/VMEIL_8h.html - 31k - [Cached](#) - [Similar pages](#)

Writing PCI Drivers: Data Structure Order

All writes to PCI configuration **space** are little **endian**. ... In short, the hardware byte **swapping** ensures that the byte order as seen by software is the ...

developer.apple.com/.../DeviceDrivers/Conceptual/

WritingPCIDrivers/endianness/chapter_7_section_5.html - 15k -

[Cached](#) - [Similar pages](#)

Source Code Guidelines

The Solaris Operating System can be used both in big-endian mode (SPARC ... The x86 platform accesses I/O ports through the I/O **address space** by means of a ...

www.sun.com/software/solaris/programs/source_code_guidelines.xml - 26k -

[Cached](#) - [Similar pages](#)

SGI TPL (IRIX 6.5: Developer/OrOn2 ProgRef - Chapter 2. M Mode ...

The vector for the cache error handler is located in Ualias **space**. The **address swapping** described above provides the cache error handler of each processor ...

techpubs.sgi.com/library/tpl/cgi-bin/getdoc.cgi/

0650/bks/SGI_Developer/books/OrOn2_ProgRef/sgi_html/ch02.html - 37k -

[Cached](#) - [Similar pages](#)

UNIX man pages : ddi_device_acc_attr (9S)

The devacc_attr_endian_flags member describes the **endian** characteristics of the ...

The size of the **address space** is 128 Kbyte with each 32 Kbyte range as a ...

bama.ua.edu/cgi-bin/man-cgi?ddi_device_acc_attr+9S - 11k - [Cached](#) - [Similar pages](#)

UNIX man pages : csx_RequestWindow (9F)

The function csx_RequestWindow() requests a block of system **address space** be assigned to a PC Card in a socket. The function csx_ReleaseWindow() releases ...

bama.ua.edu/cgi-bin/man-cgi?csx_RequestWindow+9F - 14k - [Cached](#) - [Similar pages](#)

VMEbus Architectures

Some VMEbus adapters provide hardware byte **swapping**. Digital's adapters may provide ... The **address** in the VMEbus **address space** where a data transfer occurs ...

h30097.www3.hp.com/docs/base_doc/

DOCUMENTATION/HTML/AA-Q0R7F-TE_html/CHPTR002.HTM - 30k -

[Cached](#) - [Similar pages](#)

Writing VMEbus Device Drivers

Some VMEbus adapters provide hardware byte **swapping**. ... You allocate the VMEbus **address space** for direct memory access operations by calling the following ...

h30097.www3.hp.com/docs/dev_doc/

DOCUMENTATION/HTML/AQ0R7GTE/DOCU_002.HTM - 40k -


[Cached](#) - [Similar pages](#)

DPANS94

The **address space** of an ANS Forth system is divided into an array of **address** ... For example, the following code for a 16-bit 8086 little **endian** Forth would ...

www.taygeta.com/forth/dpanse.htm - 22k - [Cached](#) - [Similar pages](#)

Google Groups results for **address space swapping endian**

 [Ordering of bytes and bits](#) - comp.arch - Apr 29, 2003

[MPC8425 vs Little **Endian** Graphics Adapter vs WindML ...](#) - comp.os.vxworks - Dec 9, 2003

[using little **endian** on sparc v9](#) - comp.unix.solaris - Mar 26, 2001

Google

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

address space swapping endian

Search

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google

[craigs.list.jason@gmail.com](#) | [Search History](#) | [My Account](#) | [Sign out](#)[Web](#) [Images](#) [Video](#) ^{New!} [News](#) [Maps](#) [more »](#)

endian conversion compiler

Search

[Advanced Search](#)
[Preferences](#)**Web**Results 1 - 10 of about **253,000** for endian conversion compiler. (0.84 seconds)**Little-endian-to-Big-endian Conversion** - 11:46am

Little-endian-to-Big-endian Conversion. The Intel Fortran **Compiler** can write unformatted sequential files in big-endian format and also can read files ...

[www.nersc.gov/vendor_docs/intel/f_ug2/cmp_endi.htm](#) - 13k - [Cached](#) - [Similar pages](#)

Programming on Davinci

The icpc command uses the same **compiler** options as the icc command. ... where type is big for big to little **endian conversion**, and little to specify data in ...

[www.nersc.gov/nusers/resources/davinci/programming.php](#) - 13k -

[Cached](#) - [Similar pages](#)

PowerPC Compiler Writer's Guide - Contents - 11:46am

3.3.2.1 Single-Precision Loads and Stores; 3.3.2.2 Double-Precision Loads and Stores; 3.3.2.3 **Endian Conversion**; 3.3.2.4 Touch Instructions ...

[wall.riscom.net/books/proc/ppc/cwg/cwg_toc.html](#) - 24k - [Cached](#) - [Similar pages](#)

CONVERT specifier - The GNU Fortran 95 Compiler

The GNU Fortran 95 **Compiler**. ... **CONVERT** works between big- and little-endian for INTEGER values of all supported kinds and for REAL on IEEE systems of kinds ...

[gcc.gnu.org/onlinedocs/gcc-4.1.1/gfortran/CONVERT-specifier.html](#) - 5k -

[Cached](#) - [Similar pages](#)

The GM API - Endian Conversion - 12:16pm

In the absence of automatic checks, **endian conversion** is typically an error-prone ... the **compiler** will report errors if any type **conversion** is missing. ...

[www.millennium.berkeley.edu/docs/mpi/gm_manual/gm_10.html](#) - 3k -

[Cached](#) - [Similar pages](#)

NCSA IBM pSeries 690: Programming Environment

IBM uses different **compiler** names to perform tasks which are handled by **compiler** ... **Endian Conversion**. The p690 and the Origin2000 machines both use big ...

[www.ncsa.uiuc.edu/UserInfo/Resources/Hardware/IBMp690/Doc/Compile.html](#) - 29k -

[Cached](#) - [Similar pages](#)

NCSA TeraGrid: Programming Environment

Little endian to big endian **conversion** ... X **compiler**) allow the display of source file traceback information at runtime when a severe error occurs. ...

[www.ncsa.uiuc.edu/UserInfo/Resources/](#)

[Hardware/TGIA64LinuxCluster/Doc/Compile.html](#) - 23k - [Cached](#) - [Similar pages](#)

ICTP - Scientific Computer Section Portal - Little-endian-to-Big ...

Intel Fortran **Compiler** · IFC User's Guide ... The little-endian-to-big-endian **conversion** feature is intended for Fortran unformatted input/output operations ...

[scs.ictp.it/manuals/ifc/f_ug/for_endi.htm](#) - 42k - [Cached](#) - [Similar pages](#)

ACES: Advanced Computing In Environmental Science

Intel **compiler**. Intel C **compiler**; Intel Fortran **compiler**; **Endian conversion** for Fortran application; File name extensions; **Compiler** options. GNU **compiler** ...
www.aces.dri.edu:8888/altix-software.jsp - 51k - [Cached](#) - [Similar pages](#)

SGI Altix User Guide: Data Conversions - 12:16pm

To use big-endian unformatted files for I/O, file format **conversion** is ... For details of the Intel Fortran **compiler**, refer to the Intel Fortran **Compiler** ...

sc.tamu.edu/help/altix/conversion.shtml - 5k - [Cached](#) - [Similar pages](#)

Google

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

Free! Speed up the web. [Download the Google Web Accelerator](#).

endian conversion compiler

Search

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google